BGU7007

SiGe:C Low Noise Amplifier MMIC for GPS, GLONASS and Galileo

Rev. 2 — 3 November 2011

Product data sheet

1. Product profile

1.1 General description

The BGU7007 is a Low Noise Amplifier (LNA) for GNSS receiver applications in a plastic leadless 6-pin, extremely small SOT886 package. The BGU7007 requires only one external matching inductor and one external decoupling capacitor.

The BGU7007 adapts itself to the changing environment resulting from co-habitation of different radio systems in modern cellular handsets. It has been designed for low power consumption and optimal performance when jamming signals from co-existing cellular transmitters are present. At low jamming power levels it delivers 18.5 dB gain at a noise figure of 0.85 dB. During high jamming power levels, resulting for example from a cellular transmit burst, it temporarily increases its bias current to improve sensitivity.

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CAUTION
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This device is sensitive to ElectroStatic Discharge (ESD). Therefore care should be taken during transport and handling.

1.2 Features and benefits

- Covers full GNSS L1 band, from 1559 MHz to 1610 MHz
- Noise figure (NF) = 0.85 dB
- Gain 18.5 dB
- High input1 dB compression point P_i (1dB) of -12 dBm
- High out of band IP3_i of 4 dBm
- Supply voltage 1.5 V to 2.85 V
- Power-down mode current consumption < 1 μA</p>
- Optimized performance at low supply current of 4.8 mA
- Integrated temperature stabilized bias for easy design
- Requires only one input matching inductor and one supply decoupling capacitor
- Input and output DC decoupled
- ESD protection on all pins (HBM > 2 kV)
- Integrated matching for the output
- Small 6-pin leadless package 1 mm × 1.45 mm × 0.5 mm
- 110 GHz transit frequency SiGe:C technology



1.3 Applications

LNA for GPS, GLONASS and Galileo in smart phones, feature phones, tablet PCs, Personal Navigation Devices, Digital Still Cameras, Digital Video Cameras, RF Front End modules, complete GPS chipset modules and theft protection (laptop, ATM)

1.4 Quick reference data

Table 1.Quick reference data

f = 1559 MHz to 1610 MHz; $V_{CC} = 1.8 \text{ V}$; $P_i < -40 \text{ dBm}$; $T_{amb} = 25 \text{ °C}$; input matched to 50 Ω using a 5.6 nH inductor; unless otherwise specified.

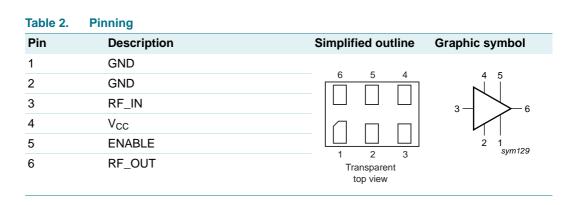
Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
V _{CC}	supply voltage	RF input AC coupled		1.5	-	2.85	V
I _{CC}	supply current	$V_{\text{ENABLE}} \geq 0.8 \text{ V}$					
		$P_i < -40 \text{ dBm}$		3.4	4.8	6.1	mA
		$P_i = -20 \text{ dBm}$		8.9	12.8	15.9	mA
Gp	power gain	P _i < –40 dBm, no jammer		16.5	18.5	20.5	dB
		$P_i = -20 \text{ dBm}$		17.5	19.5	21.5	dB
NF	noise figure	P _i < −40 dBm, no jammer	[1]	-	0.85	1.2	dB
		P _i < −40 dBm, no jammer	[2]	-	0.90	1.3	dB
		$P_i = -20 \text{ dBm}$		-	1.2	1.6	dB
P _{i(1dB)}	input power at 1 dB	f = 1559 MHz to 1610 MHz					
gain compression	V _{CC} = 1.5 V		-16	-13	-	dBm	
		V _{CC} = 1.8 V		-15	-12	-	dBm
		V _{CC} = 2.85 V		-14	-11	-	dBm
IP3 _i	input third-order intercept point	f = 1.575 GHz					
		V _{CC} = 1.5 V	[3]	1	4	-	dBm
		V _{CC} = 1.8 V	[3]	1	4	-	dBm
		V _{CC} = 2.85 V	[3]	2	5	-	dBm

[1] PCB losses are subtracted.

[2] Including PCB losses.

[3] $f_1 = 1713 \text{ MHz}$; $f_2 = 1851 \text{ MHz}$; $P_1 = P_2 = -30 \text{ dBm}$.

2. Pinning information



Product data sheet

3. Ordering information

Table 3. Orde	ering informa	tion	
Type number	Package		
	Name	Description	Version
BGU7007	XSON6	plastic extremely thin small outline package; no leads; 6 terminals; body 1 \times 1.45 \times 0.5 mm	SOT886

4. Marking

Table 4.	Marking codes	
Type num	iber	Marking code
BGU7007		B6

5. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Мах	Unit
V _{CC}	supply voltage	RF input AC coupled	-0.5	3.1	V
V _{ENABLE}	voltage on pin ENABLE	$V_{CC} \ge 2.5 \text{ V}$	-0.5	3.1	V
		V_{CC} < 2.5 V	[2] -0.5	V _{CC} + 0.6	V
$V_{RF_{IN}}$	voltage on pin RF_IN	DC			
		$V_{CC} \geq 3.0 \ V$	<u>[3]</u> –0.5	3.6	V
		$V_{CC} < 3.0 V$	[2][3] -0.5	$V_{CC} + 0.6$	V
V _{RF_OUT}	voltage on pin RF_OUT	DC			
		$V_{CC} \ge 1.8 \text{ V}$	<u>[3]</u> –0.5	3.6	V
		V _{CC} < 1.8 V	[2][3] -0.5	V _{CC} + 1.8	V
Pi	input power		-	0	dBm
P _{tot}	total power dissipation	$T_{sp} \le 130 \ ^{\circ}C$	<u>[1]</u>	55	mW
T _{stg}	storage temperature		-65	150	°C
Tj	junction temperature		-	150	°C

[1] T_{sp} is the temperature at the soldering point of the emitter lead.

[2] Due to internal ESD diode protection, the applied voltage should not exceed the specified maximum in order to avoid excess current.

[3] The RF input and RF output are AC coupled through internal DC blocking capacitors.

6. Thermal characteristics

Table 6.	Thermal characteristics			
Symbol	Parameter	Conditions	Тур	Unit
R _{th(j-sp)}	thermal resistance from junction to solder point		225	K/W

7. Characteristics

Table 7. Characteristics

f = 1559 MHz to 1610 MHz; $V_{CC} = 1.8 \text{ V}$; $V_{ENABLE} >= 0.8 \text{ V}$; $P_i < -40 \text{ dBm}$; $T_{amb} = 25 \text{ °C}$; input matched to 50 Ω using a 5.6 nH inductor; unless otherwise specified.

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{CC}	supply voltage	RF input AC coupled		1.5	-	2.85	V
I _{CC}	supply current	$V_{\text{ENABLE}} \ge 0.8 \text{ V}$					
		P _i < -40 dBm		3.4	4.8	6.1	mA
		$P_i = -20 \text{ dBm}$		8.9	12.8	15.9	mA
		$V_{\text{ENABLE}} \leq 0.35 \text{ V}$		-	-	1	μA
T _{amb}	ambient temperature			-40	+25	+85	°C
G _p	power gain	$T_{amb} = 25 \ ^{\circ}C$					
		P _i < −40 dBm, no jammer		16.5	18.5	20.5	dB
		P _i = −20 dBm, no jammer		17.5	19.5	21.5	dB
		$P_{jam} = -20 \text{ dBm}; f_{jam} = 850 \text{ MHz}$		17.5	19.5	21.5	dB
		$P_{jam} = -20 \text{ dBm}; f_{jam} = 1850 \text{ MHz}$		17.5	19.5	21.5	dB
		$-40 \text{ °C} \le T_{amb} \le +85 \text{ °C}$					
		P _i < −40 dBm, no jammer		16	-	21	dB
		$P_i = -20 \text{ dBm}$, no jammer		17	-	22	dB
		$P_{jam} = -20 \text{ dBm}; f_{jam} = 850 \text{ MHz}$		17	-	22	dB
		$P_{jam} = -20 \text{ dBm}; f_{jam} = 1850 \text{ MHz}$		17	-	22	dB
RL _{in}	input return loss	P _i < -40 dBm		5	7	-	dB
		$P_i = -20 \text{ dBm}$		7	10	-	dB
RL _{out}	output return loss	P _i < -40 dBm		12	18	-	dB
		$P_i = -20 \text{ dBm}$		15	24	-	dB
ISL	isolation			22	24	-	dB
NF	noise figure	T _{amb} = 25 °C					
		P _i < −40 dBm, no jammer	[1]	-	0.85	1.2	dB
		P _i < −40 dBm, no jammer	[2]	-	0.90	1.3	dB
		$P_i = -20 \text{ dBm}$, no jammer		-	1.2	1.6	dB
		$P_{jam} = -20 \text{ dBm}; f_{jam} = 850 \text{ MHz}$		-	1.1	1.5	dB
		$P_{jam} = -20 \text{ dBm}; f_{jam} = 1850 \text{ MHz}$		-	1.3	1.7	dB
		$-40 \text{ °C} \leq T_{amb} \leq +85 \text{ °C}$					
		P _i < −40 dBm, no jammer		-	-	1.7	dB
		P _i = −20 dBm, no jammer		-	-	1.9	dB
		P _{jam} = −20 dBm; f _{jam} = 850 MHz		-	-	1.8	dB
		$P_{jam} = -20 \text{ dBm}; f_{jam} = 1850 \text{ MHz}$		-	_	2.0	dB

Characteristics ... continued Table 7.

f = 1559 MHz to 1610 MHz; $V_{CC} = 1.8 \text{ V}$; $V_{ENABLE} >= 0.8 \text{ V}$; $P_i < -40 \text{ dBm}$; $T_{amb} = 25 \text{ °C}$; input matched to 50 Ω using a 5.6 nH inductor; unless otherwise specified.

Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
P _{i(1dB)}	input power at 1 dB gain compression	f = 1559 MHz to 1610 MHz					
		V _{CC} = 1.5 V		-16	-13	-	dBm
		V _{CC} = 1.8 V		-15	-12	-	dBm
		V _{CC} = 2.85 V		-14	-11	-	dBm
		f = 806 MHz to 928 MHz					
	V _{CC} = 1.5 V	[3]	-16	-13	-	dBm	
		V _{CC} = 1.8 V	[3]	-15	-12	-	dBm
		V _{CC} = 2.85 V	[3]	-15	-12	-	dBm
		f = 1612 MHz to 1909 MHz					
		V _{CC} = 1.5 V	[3]	-14	-11	-	dBm
		V _{CC} = 1.8 V	[3]	-13	-10	-	dBm
		V _{CC} = 2.85 V	[3]	-11	-8	-	dBm
IP3 _i	input third-order intercept point	f = 1.575 GHz					
		V _{CC} = 1.5 V	[4]	1	4	-	dBm
		V _{CC} = 1.8 V	[4]	1	4	-	dBm
		V _{CC} = 2.85 V	<u>[4]</u>	2	5	-	dBm
t _{on}	turn-on time		<u>[5]</u>	-	-	2	μS
t _{off}	turn-off time		<u>[5]</u>	-	-	1	μS
К	Rollett stability factor			1	-	-	

[1] PCB losses are subtracted.

[2] Including PCB losses.

[3] Out of band.

[4] $f_1 = 1713 \text{ MHz}$; $f_2 = 1851 \text{ MHz}$; $P_1 = P_2 = -30 \text{ dBm}$.

[5] Within 10 % of the final gain.

 Table 8.
 ENABLE (pin 5)

 -40 $^{\circ}C \leq T_{amb} \leq +85 ~^{\circ}C$; 1.5 $V \leq V_{CC} \leq 2.85 ~V$

V _{ENABLE} (V)	State
≤ 0.35	OFF
≥ 0.8	ON

8. Application information

8.1 GNSS LNA

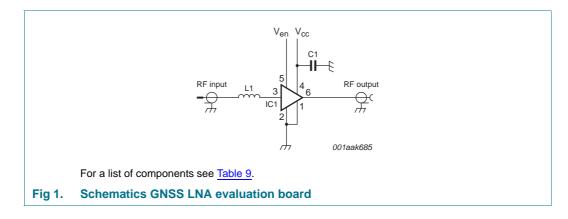
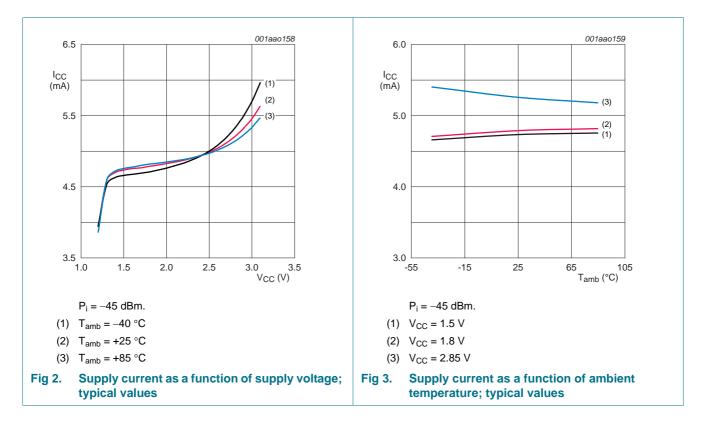


Table 9. List of components

For schematics see Figure 1.

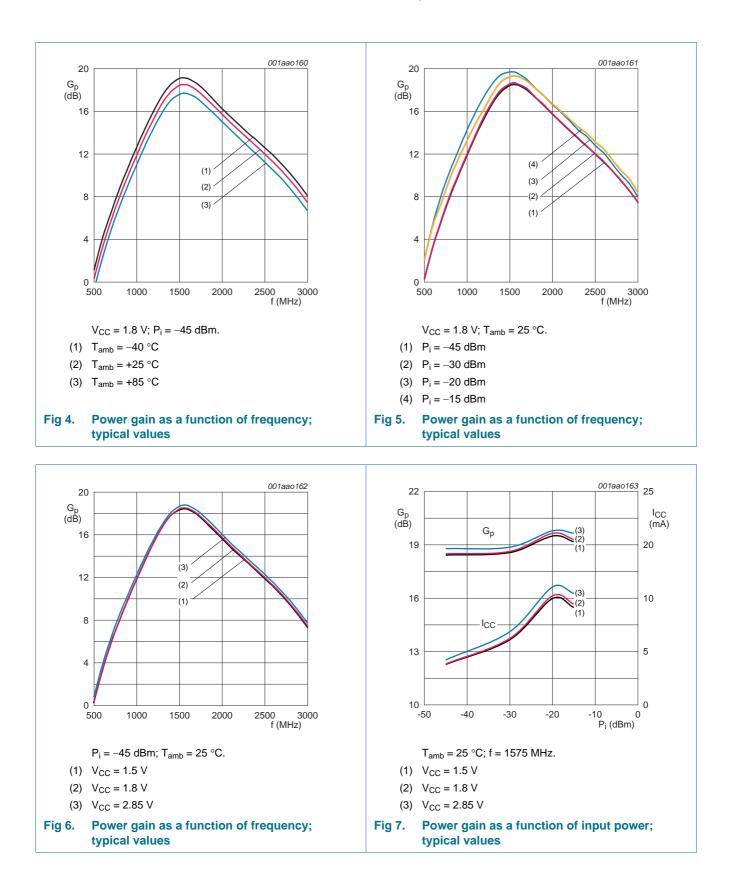
Component	Description	Value	Supplier	Remarks
C1	decoupling capacitor	1 nF	various	
IC1	BGU7007	-	NXP	
L1	high quality matching inductor	5.6 nH	Murata LQW15A	



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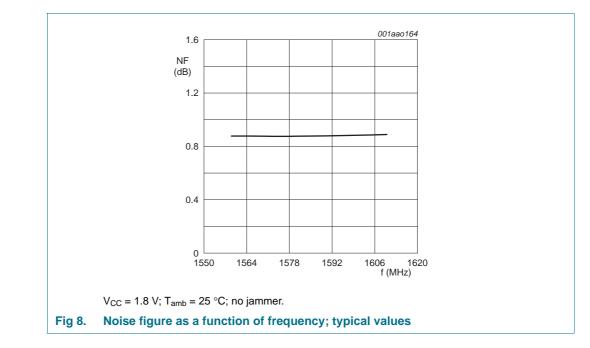
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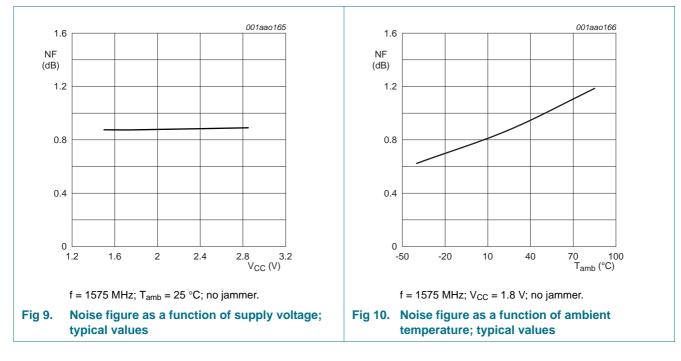
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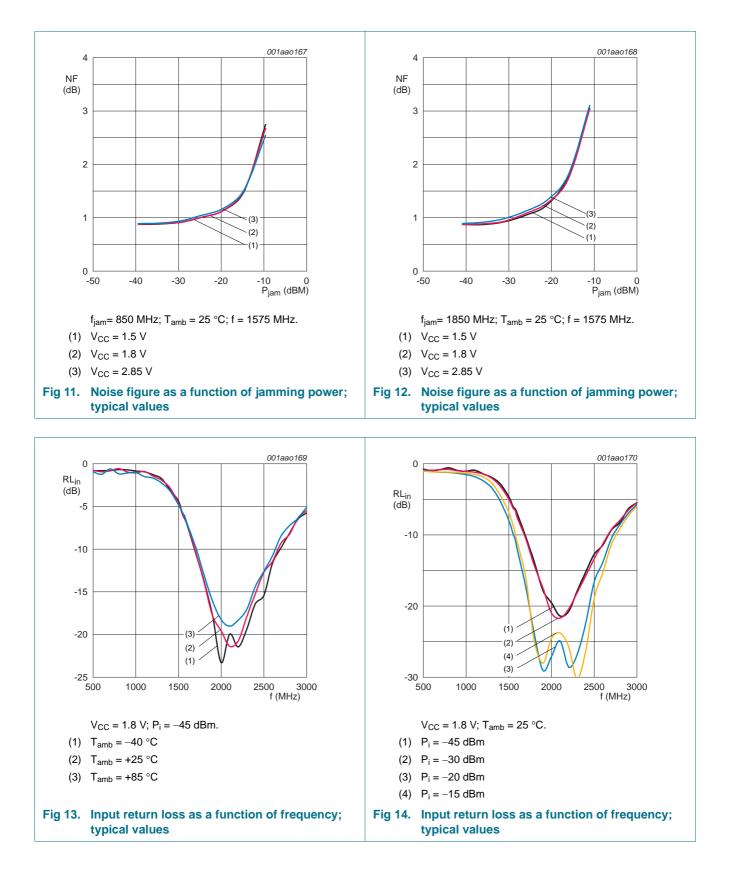




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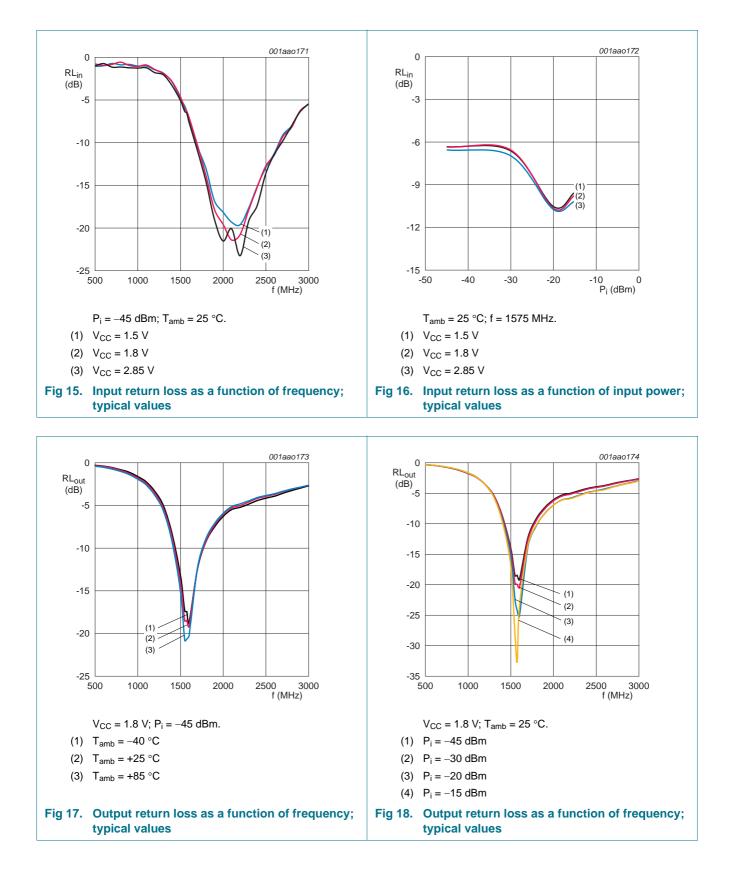
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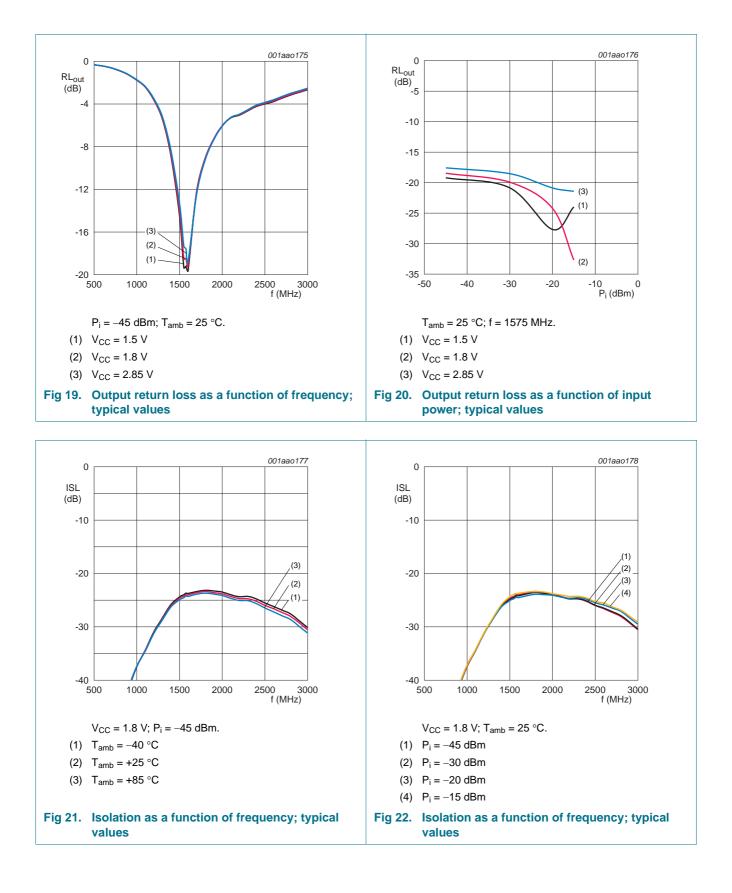
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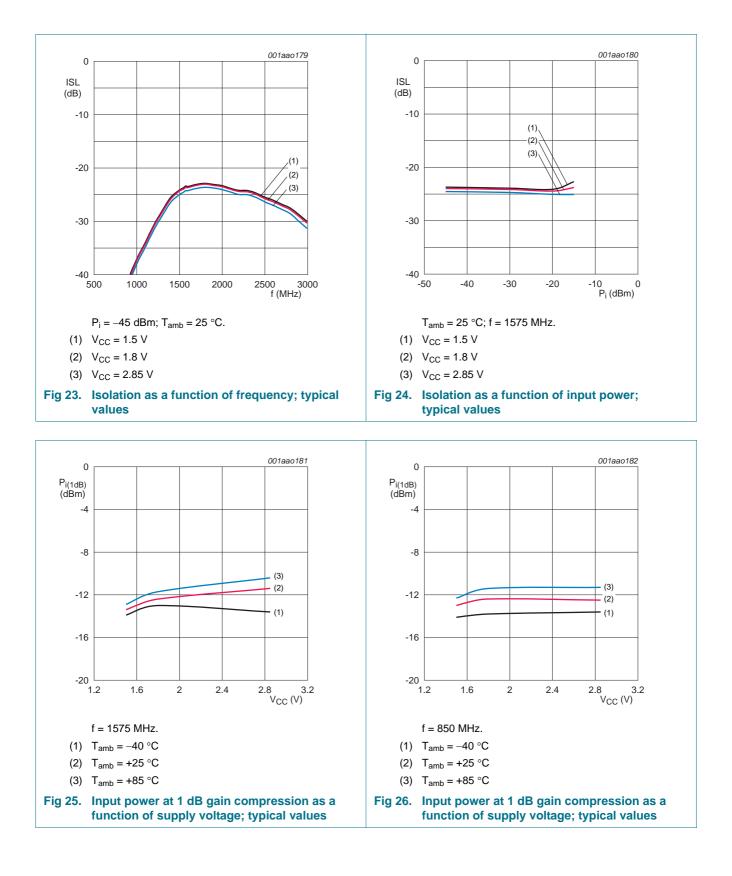
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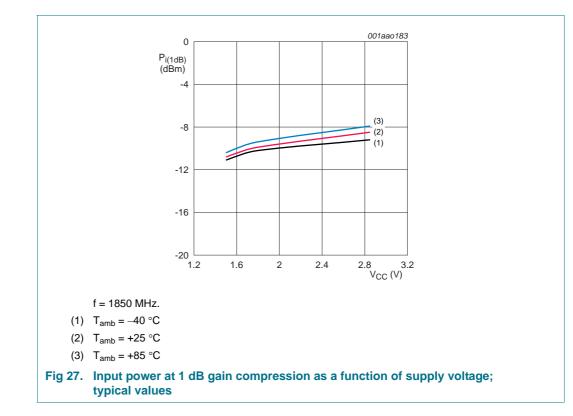
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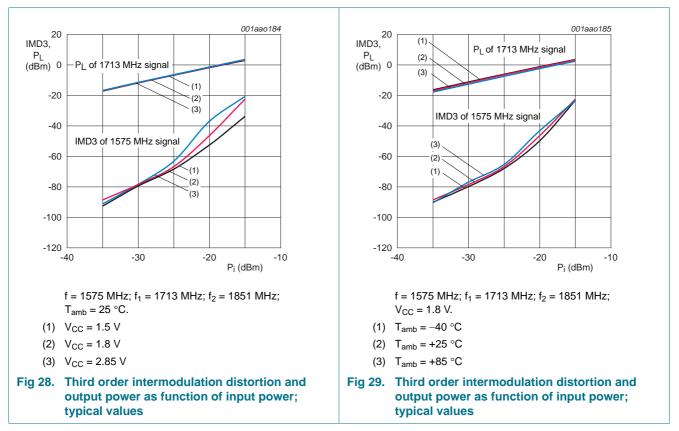
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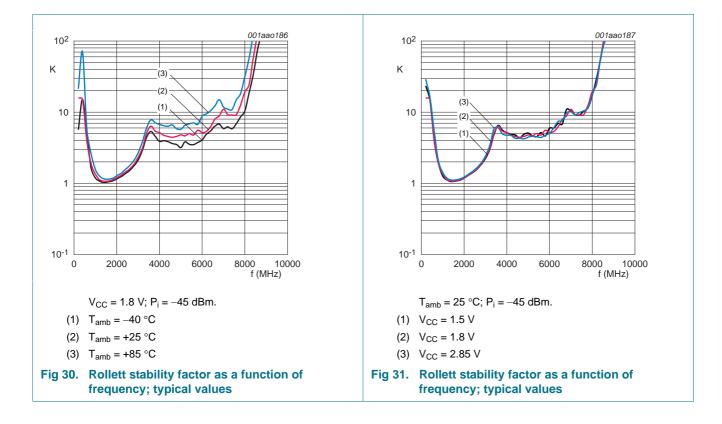




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9. Package outline

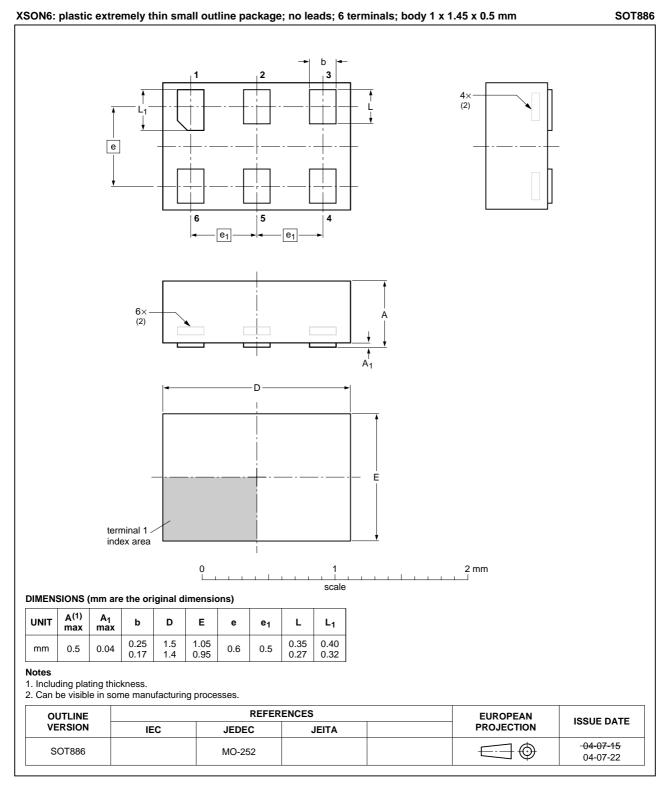


Fig 32. Package outline SOT886 (XSON6)

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10. Abbreviations

Table 10. Abbr	reviations
Acronym	Description
AC	Alternating Current
ATM	Automated Teller Machine (cash dispenser)
DC	Direct Current
GLONASS	GLObal NAvigation Satellite System
GNSS	Global Navigation Satellite System
GPS	Global Positioning System
HBM	Human Body Model
MMIC	Monolithic Microwave Integrated Circuit
PC	Personal Computer
PCB	Printed Circuit Board
RF	Radio Frequency
SiGe:C	Silicon Germanium Carbon

11. Revision history

Table 11. Revision history

-				
Document ID	Release date	Data sheet status	Change notice	Supersedes
BGU7007 v.2	20111103	Product data sheet	-	BGU7007 v.1
Modifications: Figure 25: f = 850 MHz changed to f = 1575 MHz Figure 26: f = 1850 MHz changed to f = 850 MHz Figure 27: f = 1575 MHz changed to f = 1850 MHz				
BGU7007 v.1	20110520	Product data sheet	-	-

12. Legal information

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Document status[1][2]	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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[2] The term 'short data sheet' is explained in section "Definitions".

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